



**Sital news release / NASA**

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## **NASA IS THE FIRST INTERNATIONAL CUSTOMER FOR SITAL TECHNOLOGY'S NEW MIL-STD-1553B IP CORES**

*Sital significantly improves reliability and reduces size of military standard IP cores*

Kfar Saba, Israel – November 16, 2005 – Sital Technology, a leading provider of military standard Intellectual Property (IP) cores, announced today that NASA, the National Aeronautics and Space Administration, will be using the company's MIL-STD-1553B IP Core for their deep space mission logic designs.

NASA chose Sital's product after a rigorous selection process during which it was thoroughly tested against several other competing products. It will be implemented inside a space environment hardened Field Programmable Gate Array (FPGA) which incorporates several electronic circuits to create a general purpose MIL-STD-1553B Remote Terminal.

Featuring the smallest gate count in the industry, Sital's MIL-STD-1553B IP core offers highly improved reliability over other 1553 solutions. The product connects the back-end user logic with the front-end, a standard MIL-STD-1553B Muxbus which enables real-time management by connecting all the subsystems of avionics and satellite systems.

Sital's product provides high levels of robustness, flexibility and ease-of-use and, in contrast with most cores and devices, does not require a CPU to manage the backend. It is also one of the few IP cores on the market that passed the RT Validation tests, a set of complex qualification tests required to connect to the front-end and ensure complete reliability.

“Sital's product answered NASA's requirements on several levels. They were looking for a product that would enable them to easily connect their data sources and data targets on the back-end, and would not require a CPU for managing the backend,” said Ofer Hofman, founder and R&D Manager of Sital Technology. “NASA's decision to use an IP core, rather than a discrete device, is a significant milestone in the 20 year history of the MIL-STD-1553B standard. It signals an industry-wide trend towards using the more reliable and easily customizable IP cores for FPGAs instead of the expensive and inflexible single source 1553 IC chips.”

Sital's IP cores are available for Remote Terminal and Bus Monitor applications. An IP core for Bus Controller applications will be available in Q1 2006. Sital's business model enables the company to customize pricing models per the customer's specific needs.

### **About Sital Technology**

Founded in 1993, Sital Technology is a leading provider of EDA (Electronic Design Automation) products and services. Sital combines representation of leading vendors such as Mentor Graphics, Opnet and Cliosoft, training for design languages (VHDL, Verilog, PSL, SystemVerilog), and a high-level R&D center for Military communications, Network design, DSP and Image Processing.



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SITAL Technology's key quality resource is its creative, talented and professional staff. Sital's engineers are veterans of the Israeli Air Force, who served in the technical units of the F-16 avionics systems. They gained knowledge and experience with the Mil-Std-1553 standard from the bottom up, both as design engineers for Mil-Std-1553 components and as technicians working on the aircrafts.

Among Sital's many customers you can find NASA, Israeli Aircraft Industries (IAI), Rafael, Elbit, Astronautics, Tadiran, Israeli Ministry of Defense, Elta, ITL Optronics, BAE Systems, RADA and many others.

For more information about Sital and its products, please visit our web site at [www.sital.co.il](http://www.sital.co.il) or contact Duli Yariv, Sital's VP Sales & Marketing, by phone at (+972) 52-2482332 or via email at [duli@sital.co.il](mailto:duli@sital.co.il).